

Simultaneous Test of Graphic Recorders (KR-2000 and KR-3000) with I/O Stimulator

Jang-Yeol Kim, Chang-Hwoi Kim

*Korea Atomic Energy Research Institute(KAERI)
989-111 Daedeok-daero, Yuseong-gu, Daejeon, Republic of Korea 305-353
jykim@kaeri.re.kr; chkim2@kaeri.re.kr*

1.0 INTRODUCTION

A nuclear safety-grade graphic recorder is a microprocessor-based digital recorder that provides detailed operation information to operators of nuclear power plants. A nuclear safety-grade graphic recorder display on a 5.6 inch LCD screen the operation information such as input data (temperature, pressure, flow, power, and water level) from physical quantities directly. A real-time display can be possible for bar graphs, trends and other form of information. It also stores the measurement data in its own external storage device. The past recorded data can be retrieved at the required time and the trend of the past and present values can be displayed. The data can be compared and analyzed simultaneously on the same screen. Typical main functions are the display, alarm, arithmetic, setting, recording and other functions. A nuclear safety-grade graphic recorder shall meet the following functional and performance requirements.

- o Screen updated once per second
- o Within 125ms (scan and sampling)
- o Accuracy: ± 0.1 Full Range
- o Ensure safety of recorded data
- o Data collection within 10 seconds in case of power supply fail.

In addition, the nuclear-safety grade graphic recorder shall satisfy the following three safety function attributes.

- o Unknown Event Monitoring using Watch Dog Timer(WDT)
- o Self diagnostic and alarm output
- o Administrator password and security function

We have automatically tested whether the main functions of the original graphic recorder are operating correctly by using an I/O simulator, which were previously tested by one by one. At this time, two models were tested at the same time. The first model is the KR-2000 graphic recorder and the second model is the KR-3000 graphic recorder. Both were tested at the same time; additionally, the testbed setup, the test case, the test configuration, and cabling were setup under the same conditions. The I/O Simulator is an NI-PXI-based analog, digital I/O system, with each input/output module mounted on a PXI system and wired with an SCB-68 connector block. For the automatic test, the commercial LabVIEW application program was used and the input and output each module were controlled and measured. The KR-2000 recorder has a total of 12 channels. The KR-3000 recorder has a total of 48

channels. Simultaneous test was carried out for a total of 60 channels under the 1S cycle triangle signal generation during 24 hours continuously. The test conditions used were equal to the recording cycle and the accuracy of commercial nuclear power plants.

2.0 Simultaneous target test with I/O Simulator

2.1 Hardware Configuration

I/O Simulator test equipment is required for simultaneous target testing of KR-2000 graphic recorder and KR-3000 graphic recorder. The I/O Simulator test equipment is the same as the one that performed the FLC (for example, FPGA-based Logic Controller[1]) system test before because the difference in the subject, re-configuration such as testbed setup, test case, test configuration and cabling are required. The I/O simulator is an NI-PXI-based analog, digital input and output system, with each input and output module mounted on a PXI system and wired with an SCB-68 connector block. We used commercial LabVIEW application and controlled the input and output of each module. The I/O simulator test device main component configuration details are shown in Table 1 and the hardware configuration diagram is shown in Figure 1.

Table1. Configurations of I/O Simulator Test Device Major Parts

Items	Quantity	Comments
NI-PXIe 8135	1EA	2.3 GHz Quad-Core PXI Express Controller
NI-PXIe 1075	1EA	8 hybrid slots, 8 PXI Express slots, and 1 PXI Express system timing slot embedded chassis
NI-PXI 6511	1EA	64 channel sync / source digital input
NI-PXI 6512	1EA	64 channel source digital output
NI-PXIe 6363	2EA	32 channel analog input
NI-PXI 6239	1EA	8-channel high-speed analog input

NI-PXI 6733	1EA	8-channel high-speed analog output
NI-PXI 6704	1EA	16 channel voltage, current output
SCB-68	4EA	I/O connector block for DAQ devices with 68-pin connector

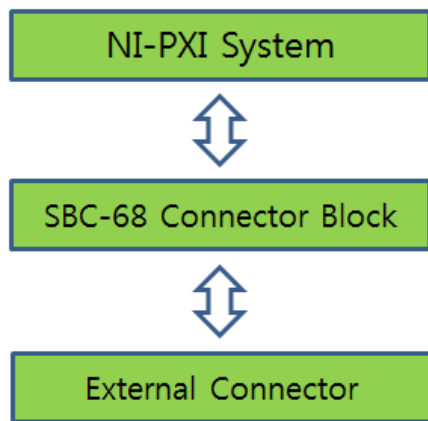


Fig. 1. Hardware Configuration Diagram

2.2 Software Configuration

We created an application program based on the commercial LabVIEW program. The application program executes the function of control and measurement based on each I/O module connected to the PXI. They also generate signals and store the result of the measured data.

The signal is generated by both a closed loop and an open loop. An internal signal source and an external signal source are shown in Figure 2 and Figure 3, respectively

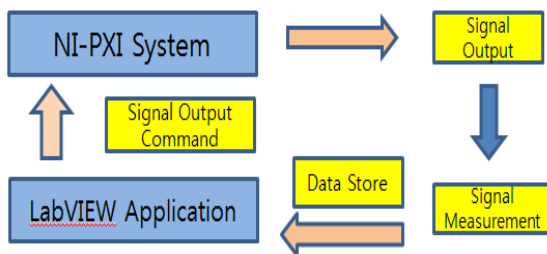


Fig. 2. I/O Stimulator Output from inside source

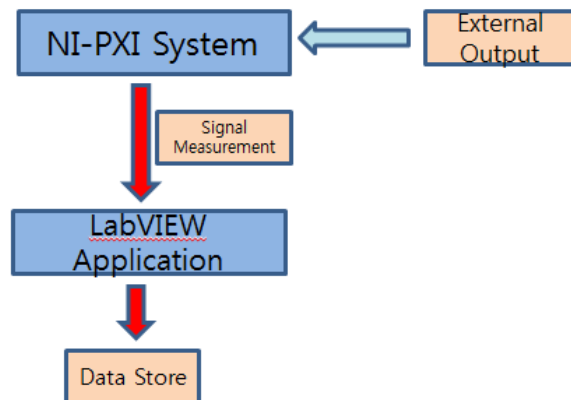


Fig. 3. I/O Stimulator Output from outside source

2.3 Target Testing

KR-2000 and KR-3000 using an I/O stimulator have been tested. Test results were also recorded under the same conditions with Yokogawa. The two target records and referenced record of Yokogawa are recorded simultaneously. After the test is done, it was compared two target records and reference recorder (Yokogawa). One channel signal source from I/O stimulator assigned to four target channels, which means KR-2000 were connected three groups (4x3=12), KR-3000 were connected 12 groups (4X12=48). Twelve (12) and three (3) equals fifteen (15). We used fifteen signal sources from I/O stimulator. Target test graphic recorders (KR-2000 model and KR-3000 model) connected with I/O stimulator are shown in Figure 4. The KR-2000 recorder has a total of 12 channels and the KR-3000 recorder has a total of 48 channels. Simultaneous test was carried out for a total of 60 channels about a 1 cycle triangle signal generation for 24 hours continuously. The test conditions were setup equal to the recording cycle and accuracy of domestic commercial nuclear power plants. The connection diagram of the target items is shown in Figure 5.

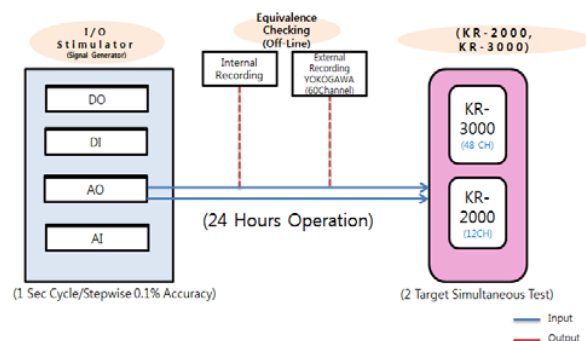


Fig. 4. Connection diagram between target test recorder (KR-2000 and KR-3000 with I/O Stimulator)

The input tag signal from KR-2000 and KR-3000 were connected 15 cabling groups from an I/O stimulator because numbers of signals were so many. One channel signal source from I/O stimulator assigned to four target channels, which means KR-2000 were connected three group ($4 \times 3 = 12$), KR-3000 were connected 12 group ($4 \times 12 = 48$). Twelve (12) and three (3) equals fifteen (15). Fifteen means total signal sources from I/O stimulator as shown in Figure 5.



Fig. 5. Simultaneous testing of KR-2000 recorder and KR-3000 recorder

3.0 Results

The result of the simultaneous continuous test of KR-2000 graph recorder and KR-3000 graph recorder is shown in Figure 6. It can get result values from comparison result value of external recording device. Both were tested PASS under 1S triangle signal generation successfully. The left screen in Figure 6 is the result of the target KR-2000 and KR-3000.

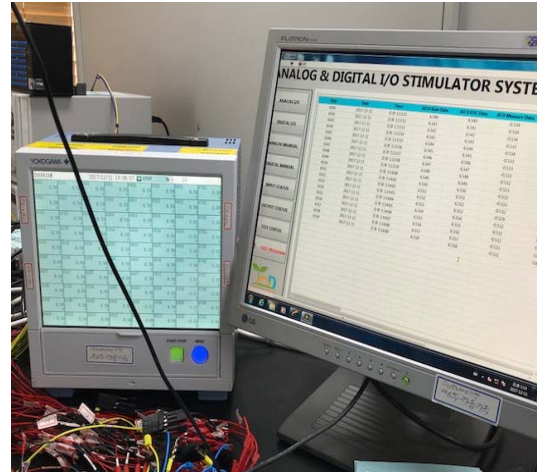


Fig. 6. Test result screen of KR-2000 recorder and KR-3000 recorder

4.0 Conclusion

The test conditions setup were equal to the recording cycle and the accuracy of commercial nuclear power plants. The KR-2000 graphic recorder and the KR-3000 graphic recorder were tested and verified to record consecutively for a long term period of time under 0.1% accuracy at 1 second. Further tests will be made at 100ms condition.

NOMENCLATURE

AI : Analog Input
 AO : Analog Output
 DI : Digital Input
 DO : Digital Output
 I/O : Input/Output
 KR : Korea
 LCD : Liquid Crystal Display
 NI-PXI : National Instruments-PCI eXtensions for Instrumentation
 SCB-68 : Shielded I/O Connector Block

ACKNOWLEDGMENT

This work, described herein, is being performed for the "Development and Operation of ICT-based Nuclear Energy Safety Validation System," as a part of the Korea Atomic Energy Research Institute (KAERI) projects and funded by Ministry of Science and ICT since on January 1st, 2018. (Project Number: 524320-18)

REFERENCES

1. Jang-Yeol Kim, Dong-Young Lee, Chang-Hwoi Kim, Kee-Choon Kwon, Jang-Soo Lee, Ji-Yeon Park, "An automatic Input/Output Test Experience of FPGA based Logic Controller with I/O Stimulator ", NPIC&HMIT 2017, San Francisco, CA, June 11-15, 2017
2. Jangyeol Kim and Soongohn Kim, "Qualification Paradigm for Independent Software Verification and Validation", International Journal of Software Engineering and Its Applications, Vol.8, No.3 (2014), pp.305-312
3. Jang-Yeol Kim, Soon-Gohn Kim, "Software Qualification Approach for Safety-critical Software of the Embedded System", The 2012 International Conference on Future Generation Communication and Networking (FGCN), Kangwondo Korea, December 16-19, 2012
4. J. Y. Kim, Kee-Choon Kwon, "The Commercial Off The Shelf(COTS) Dedication of QNX Real Time Operating System(RTOS)," International Conference on Reliability, Safety and Hazard-2010, Mumbai India, December 14-16, 2010.
5. J.Y. Kim, S.W. Cheon, J.S. Lee, Y.J. Lee, K.H. Cha, and Kee-Choon Kwon, "Software V&V Methods for a Safety Grade Programmable Logic Controller," International Conference on Reliability, Safety and Hazard-2005, Mumbai India, December. 1-3, 2005.
6. 10CFR 50 Appendix A,4/94, "General Design Criteria"
7. ASME NQA-1-1997 "Quality Assurance Requirements for Nuclear Facility Applications"
8. NUREG-0800, Mar. 2007, "Standard Review Plan (Ch. 7): Instrumentation and Controls."
9. IEC 62566, Jan.2012, "Nuclear power plants—Instrumentation and control important to safety—Development of HDL-programmed integrated circuits for systems performing category A functions "
10. USNRC Reg. Guide 1.152, Rev. 02, 2006, "Criteria for Programmable Digital Computers System Software in Safety Related Systems of Nuclear Power Plants"
11. USNRC Reg. Guide 1.172, Rev. 00, Jul. 1997, "Software Requirements Specifications for Digital Computer Software Used in Systems of Nuclear Power Plants"
12. IEEE Std. 7-4.3.2-2003, "Standard Criteria for Digital Computers in Safety System of Nuclear Power Generating Stations"
13. IEEE Std. 829-1998, "IEEE Standard for Software Test Documentation"
14. IEEE Std. 1008-1987, "IEEE Standard for Software Unit Testing"
15. IEEE Std. 1012-1998, "IEEE Standard for Software verification and validation"
16. IEEE Std-1016, "IEEE Recommended Practice for Software Design Descriptions," 1998.